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Application No.	<u>091854,905</u>	Prepared by	<u>N.H.</u>	Tracking Number
Examiner-GAU	<u>Pert - 2829</u>	Date	<u>2-2-04</u>	Week Date
		No. of queries	<u>1</u>	<u>IFW(E)</u>

JACKET

a. Serial No.	f. Foreign Priority	k. Print Claim(s)	p. PTO-1449
b. Applicant(s)	g. Disclaimer	l. Print Fig.	q. PTOL-85b
c. Continuing Data	h. Microfiche Appendix	m. Searched Column	r. Abstract
d. PCT	i. Title	n. PTO-270/328	s. Sheets/Figs
e. Domestic Priority	j. Claims Allowed	o. PTO-892	t. Other

SPECIFICATION

- a. Page Missing
- b. Text Continuity
- c. Holes through Data
- d. Other Missing Text
- e. Illegible Text
- f. Duplicate Text
- g. Brief Description
- h. Sequence Listing
- i. Appendix
- j. Amendments
- k. Other

MESSAGE

PTO-1449! Please either initial or line through citations. Copies provided for reference.

CLAIMS

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- k. Other

C *Thank you*

initials *N.H.*

RESPONSE

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FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE				ATTY. DOCKET NO.	SERIAL NO.	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				11157-23	09/854,905	
(Use several sheets if necessary)				APPLICANT		
				BRIAN MOORE		
				FILING DATE	GROUP	
				May 15, 2001	2858	
U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPLICABLE
	3,689,885	09/05/72	Kaplan et al.	340	152T	09/15/70
	4,002,974	01/11/77	Thomas	324	73 R	11/13/75
	4,105,950	08/08/78	Dingwall	331	57	09/12/77
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	5,030,908	07/09/91	Miyoshi et al.	324	158 R	01/23/
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	5,457,400	10/10/95	Ahmad et al.	324	763	07/23/93
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	5,995,428	11/30/99	Chien et al.	365	201	02/27/98
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	DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
v	0098399	06/04/86	Europe	G 01 R 31	28	Not needed
r	0911640	04/28/99	Europe	G 01R 29	027	Not needed
v	99/16107	04/01/99	PCT	H01L	—	Not needed
v	99/32893	07/01/99	PCT	G01R 31	00	Not needed
v	99/65287	12/23/99	PCT	N/a	N/a	Not needed

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

Introduction to Component Testing – by Anthony K. Stevens – 1986 – pages 3-14 & pages 91-119
Semiconductor Material and Device Characterization – by Dieter K. Schroder – 1990 – pages 1-40
CMOS Digital Integrated Circuits – by Sung-Mo (Steve) Kang & Yusuf Leblebici – 1999 – pages 220-242, Chaps 6, 9, 11, 15
A Self-Tuning Digital Telemetry IC for Use in a Microprocessor-Based Implantable Instrument – IEEE Journal of Solid-State Circuits – Vol. 27, No 12 (Dec. 1992) - by Kenneth W. Fernald, John J. Paulos, Blaine A. Stackhouse & Robert A. Heaton – Pages 1826-1832
Testing the Monster Chip – IEEE Spectrum, (July 1999) – by Yervant Zorian – Pages 54-60

DATE CONSIDERED

EXAMINER:

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Substitute for form 1449A/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Application Number	09/854,905
(use as many sheets as necessary)		Filing Date	May 15, 2001
		First Named Inventor	MOORE, Brian
		Group Art Unit	2829
		Examiner Name	N/A
		Attorney Docket Number	11157-23
Sheet	1	of	1

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Signature	Jimmy Brown	Date Considered	6/4/03
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